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PATENT COOPERATION Th_ATY

	From the INTERNATIONAL BUREAU
PCT	То:
NOTIFICATION OF ELECTION (PCT Rule 61.2)	Assistant Commissioner for Patents United States Patent and Trademark Office Box PCT Washington, D.C.20231 ETATS-UNIS D'AMERIQUE
Date of mailing (day/month/year) 06 September 2000 (06.09.00)	in its capacity as elected Office
International application No.	Applicant's or agent's file reference
PCT/GB99/04245	PDG/21095
International filing date (day/month/year) 15 December 1999 (15.12.99)	Priority date (day/month/year) 15 December 1998 (15.12.98)
Applicant	
ROBERTSON, Paul, Gordon	
1. The designated Office is hereby notified of its election made X In the demand filed with the International Preliminary 11 July 2000 (In a notice effecting later election filed with the International Preliminary 2. The election X was Was not was not made before the expiration of 19 months from the priority Rule 32.2(b).	y Examining Authority on: 11.07.00) national Bureau on:
The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20. Switzerland	Authorized officer Juan Cruz

Telephone No.: (41-22) 338.83.38

Facsimile No.: (41-22) 740.14.35 Form PCT/IB/331 (July 1992)

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INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

	or agent's file reference	FOR FURTHER ACTION	See Notification of Transmittal of International
PDG/210	95	TOTT OTTILL AGRICA	Preliminary Examination Report (Form PCT/IPEA/416)
	al application No.	International filing date (day/month	
PCT/GB9		15/12/1999	15/12/1998
Internationa H04N7/1		or national classification and IPC	
Applicant SNELL &	WILCOX LIMITED et a	d.	
1. This i and is	nternational preliminary eartransmitted to the application	kamination report has been prepared ant according to Article 36.	I by this International Preliminary Examining Authority
2. This F	REPORT consists of a total	al of 7 sheets, including this cover s	neet.
b (\$	een amended and are the	e basis for this report and/or sheets on 607 of the Administrative Instructi	e description, claims and/or drawings which have ontaining rectifications made before this Authority ons under the PCT).
3. This r	eport contains indications Basis of the report	relating to the following items:	
11	□ Priority		
101	☐ Non-establishment	of opinion with regard to novelty, inv	rentive step and industrial applicability
IV	☑ Lack of unity of inv	ention	
V		nt under Article 35(2) with regard to nations suporting such statement	novelty, inventive step or industrial applicability;
VI	☐ Certain document	s cited	
VII	⊠ Certain defects in t	he international application	
VIII	⊠ Certain observation	ns on the international application	
Date of sub	mission of the demand	Date of	completion of this report
11/07/20	00	06.02.2	001
	mailing address of the international examining authority:	tional Authoriz	ed officer
<u>a</u>	European Patent Office D-80298 Munich Tel. +49 89 2399 - 0 Tx: 52	de Die	uleveult, A
	Fax: +49 89 2399 - 4465		ne No. +49.89 2399 8946

INTERNATIONAL PRELIMINARY **EXAMINATION REPORT**

International application No. PCT/GB99/04245

I. Basi:	of the	report
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	bas	is of the report				
1.	resp the	oonse to an invitati	Irawn on the basis of (substitut on under Article 14 are referred to not contain amendments (Re	d to in this repo	ort as "originally file	hed to the receiving Office in ed" and are not annexed to
	1-10)	as originally filed			
	Clai	ims, No.:				
	14-2	22	as originally filed			
	1-13	3	as received on	11/01/2001	with letter of	08/01/2000
	Dra	wings, sheets:				
	1/4-	4/4	as originally filed			
2.	With	n regard to the lang guage in which the	guage, all the elements marke international application was fi	d above were a led, unless oth	available or furnish erwise indicated u	ed to this Authority in the nder this item.
	The	se elements were	available or furnished to this A	uthority in the f	ollowing language	: , which is:
		the language of a	translation furnished for the pu	urposes of the i	international searc	h (under Rule 23.1(b)).
		the language of pr	ublication of the international a	pplication (und	er Rule 48.3(b)).	
		the language of a 55.2 and/or 55.3).	translation furnished for the pu	urposes of inter	rnational prelimina	ry examination (under Rule
3.			cleotide and/or amino acid sory examination was carried out			
		contained in the ir	nternational application in writte	en form.		
		filed together with	the international application in	computer read	dable form.	
		furnished subsequ	uently to this Authority in writte	n form.		
		furnished subsequ	uently to this Authority in comp	uter readable f	orm.	
			at the subsequently furnished vapplication as filed has been fu		ce listing does not	go beyond the disclosure in
		The statement the	at the information recorded in o	omputer reada	ble form is identica	al to the written sequence

Form PCT/IPEA/409 (Boxes I-VIII, Sheet 1) (July 1998)

4. The amendments have resulted in the cancellation of:

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No. PCT/GB99/04245

		the description,	pages:								
		the claims,	Nos.:								
		the drawings,	sheets:								
5.		This report has been considered to go bey						d not been r	nade, sin	ce they ha	ve been
		(Any replacement sh report.)	neet contain	ing such	amend	lments mus	t be referi	red to under	item 1 ar	nd annexe	d to this
6.	Add	litional observations, i	f necessary	<i>/</i> :							
IV.	Lac	k of unity of invention	on								
1.	In re	esponse to the invitati	on to restri	ct or pay	addition	nal fees the	applicant	has:			
		restricted the claims.									
		paid additional fees.									
		paid additional fees	under prote	st.							
	☒	neither restricted no	r paid additi	onal fees	•						
2.		This Authority found 68.1, not to invite the						complied and	d chose, a	according	to Rule
3.	This	s Authority considers	that the req	uirement	of unity	y of invention	on in acco	rdance with	Rules 13	.1, 13.2 ar	nd 13.3 i
		complied with.									
	×	not complied with for see separate sheet		ng reasoi	ns:						
4.		nsequently, the follow mination in establishi			national	l application	n were the	subject of i	nternation	nal prelimi	nary
		all parts.									
	×	the parts relating to	claims Nos.	1-8,22.							
٧.		asoned statement ur ations and explanation					elty, inve	ntive step c	or industr	rial applic	ability;
1.	Sta	tement									
	Nov	velty (N)	Yes:	Claims	1-8,22	2					



International application No. PCT/GB99/04245

Inventive step (IS)

Yes: Claims

Claims 1-8,22

Industrial applicability (IA)

Yes: Claims

Claims 1-8,22

No: Claims

No:

2. Citations and explanations see separate sheet

VII. Certain defects in the international application

The following defects in the form or contents of the international application have been noted: see separate sheet

VIII. Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made: see separate sheet

INTERNATIONAL PRELIMINARY InteREXAMINATION REPORT - SEPARATE SHEET

IV. Lack of unity

The separate groups of invention are:

- I. Claims 1-8, 22 (main invention)
- II. Claims 9-17
- III. Claim 18
- IV. Claims 19, 20
- V. Claim 21

They are not so linked as to form a single general inventive concept (Rule 13.1 PCT) for the following reasons:

- 1. Claims 1 and 8 are directed to a digital video signal processor for stripping timing reference signals (TRSs) from the digital video signal and inserting other timing reference signals at other locations.
 - Claim 22 is directed to the corresponding video signal.
 - NB: it is not quite clear which "scrambled" video signal is meant when referring back to claim 1.
- Claim 9 deals with a digital video interface wherein TRSs are transmitted less than twice per line.
 - NB: this claim appears not to be supported by the description. Besides, the way TRSs are defined with reference to digital video standards other than Recommendation 656 is not clear.
- 3. Claim 18 is directed to a digital video interface for scrambling data words wherein specific words are re-scrambled until a valid word is obtained.
- 4. Claim 19 is directed to a digital video interface for scrambling data words wherein specific words are replaced with unscrambled words.
- 5. Claim 21 deals with a digital video processor for inserting into the video signal identical TRSs appearing once per picture.
 - NB: no support is to be found in the description for this claim either.

V. Reasoned statement

1. Reference is made to the following document:

D1: US-A-5 757 910 (RIM) 26 May 1998

2. Claim 1:

Document D1 discloses (see column 3, line 47 - col. 5, l. 46) a digital video processor (120-150) having an input adapted to receive a digital video signal (output from A/D converter 110) and a timing reference processor (130) for inserting timing signal references into the video signal (see the paragraph bridging columns 4 and 5).

Furthermore, it is well known to the skilled person that a video signal (analog or digital) normally comprises first timing signal references at fixed first locations within the line and picture structure (see for instance the "horizontal and vertical synchronous signals" in col. 1, l. 18-23 of D1) and that, for the purpose of scrambling, a timing reference stripper is required to remove said first timing signal references from the video signal (see col. 1, l. 52-57) before said timing reference processor can insert (second) timing signal references into the video signal at locations other than said first locations.

Whether such a timing reference stripper processes an analog or a digital video signal, these are obvious alternatives for the skilled person who would select one or the other, in accordance with circumstances, without the exercise of inventive skill, in order to scramble the video signal.

Consequently, the claimed subject-matter is considered to lack an inventive step with respect to the disclosure of D1.

3. Claim 8:

D1 further discloses a digital video processing system (see figures 3 and 4 together) comprising, in addition to an output component according to claim 1, an input component (210-270) adapted to receive (see antenna A) a digital video signal having said second timing signal references, to remove (230) said second timing signal references and to derive (250) from said second timing signal references appropriate timing references for use in further processing (260) of the digital video signal.

This claim thus does not satisfy the requirements of Article 33(3) PCT either.

4. Claim 22:

The same objection likewise applies to the video signal itself.

INTERNATIONAL PRELIMINARY International application No. PCT/GB99/04245 EXAMINATION REPORT - SEPARATE SHEET

5. Claims 2-7:

These dependent claims do not appear to comprise any additional features that would render their subject-matter inventive over the available prior art.

Therefore, these claims fail together with the independent claims for lack of inventive step.

VII. Certain defects

- 1. The independent claims are not in the two-part form in accordance with Rule 6.3(b) PCT, which in the present case would be appropriate, with those features known in combination from the prior art (document D1) being placed in the preamble (Rule 6.3(b)(i) PCT) and with the remaining features being included in the characterising part (Rule 6.3(b)(ii) PCT).
- 2. The features of the claims are not provided with reference signs placed in parentheses (Rule 6.2(b) PCT).
- 3. Contrary to the requirements of Rule 5.1(a)(ii) PCT, the relevant background art disclosed in the document D1 is not mentioned in the description, nor is this document identified therein.

VIII. Certain observations

- 1. Claims 1 through 8 comprise "timing signal references" whereas the description refers to "timing reference signals (TRSs)".
- 2. Claim 22 does not meet the requirements of Article 6 because it is not clear which "scrambled" video signal is meant when referring back to claims 1 and 8.

CLAIMS

- 1. A digital video signal processor having an input adapted to receive a digital video signal having first timing signal references at fixed first locations within the line and picture structure; a timing reference stripper for removing said first timing signal references from the digital video signal and a timing reference processor for inserting second timing signal references into the video signal at locations other than said first locations.
- A processor according to Claim 1, wherein said second timing signal references are substantially fewer in number than said first timing signal references.
- A processor according to Claim 1, wherein said first timing signal references comprise start of line and end of line references and said second timing signal references are inserted substantially once per picture.
- 4. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the number of lines per picture within the digital video signal.
- 5. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the length of each line within the digital video signal.
- 6. A processor according to any one of the preceding claims, wherein substantially every second timing signal reference includes information concerning the aspect ratio of the picture.

- 7. A processor according to any one of the preceding claims, wherein substantially all the second timing signal references are identical.
- 8. A digital video signal processing system comprising an output component adapted to receive a digital video signal having first timing signal references at fixed first locations within the line and picture structure, to remove said first timing signal references and insert second timing signal references at locations other than said first locations; and an input component adapted to receive a digital video signal having said second timing signal references, to remove said second timing signal references and to derive from said second timing signal references appropriate timing references for use in further processing of the digital video signal.
- 9. A digital video interface substantially in accordance with ITU/R Recommendation 656, or other digital video standard, characterised in that timing reference signals are transmitted less than twice per line in order to inhibit unauthorised use of the video information.
- 10. A digital video interface as described in Claim 9 where no timing reference signal corresponds to the start or finish of a digital active line.
- 11. A digital video interface in accordance with either Claim 9 or Claim 10 in which the timing reference signals are identical.
- 12. A digital video interface in accordance with either Claim 9 or Claim 10 in there is no explicit F, V and H information in the timing reference signals.
- 13. A digital video interface in accordance with any one of Claims 9 to 12, in which aspect ratio information is carried in the timing reference signals.



- 11 -

CLAIMS

- 1. A digital video signal processor having an input adapted to receive a digital video signal having first timing signal references at fixed locations within the line and picture structure; a timing reference stripper for removing said first timing signal references from the digital video signal and a timing reference processor for inserting second timing references into the video signal at locations other than said first locations.
- 2. A processor according to Claim 1, wherein said second timing references are substantially fewer in number than said first timing references.
- A processor according to Claim 1, wherein said first timing signal references comprise start of line and end of line references and said second timing references are inserted substantially once per picture.
- 4. A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the number of lines per picture within the digital video signal.
- 5. A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the length of each line within the digital video signal.
- A processor according to any one of the preceding claims, wherein substantially every second timing reference includes information concerning the aspect ratio of the picture.

WO 00/36838 PCT/GB99/04245

- 12 -

- 7. A processor according to any one of the preceding claims, wherein substantially all the second timing references are identical.
- 8. A digital video signal processing system comprising an output component adapted to receive a digital video signal having first timing signal references at fixed locations within the line and picture structure, to remove said first timing signal references and insert second timing references at locations other than said first locations; and an input component adapted to receive a digital video signal having said second timing signal references, to remove said second timing signal references and to derive from said second timing references appropriate timing references for use in further processing of the digital video signal.
- 9. A digital video interface substantially in accordance with ITU/R Recommendation 656, or other digital video standard, characterised in that timing reference signals are transmitted less than twice per line in order to inhibit unauthorised use of the video information.
- 10. A digital video interface as described in Claim 9 where no timing reference signal corresponds to the start or finish of a digital active line.
- 11. A digital video interface in accordance with either Claim 9 or Claim 10 in which the timing reference signals are identical.
- A digital video interface in accordance with either Claim 9 or Claim 10 in there is no explicit F, V and H information in the timing reference signals.
- 13. A digital video interface in accordance with any one of Claims 9 to 12, in which aspect ratio information is carried in the timing reference signals.

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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(72) Inventor; and

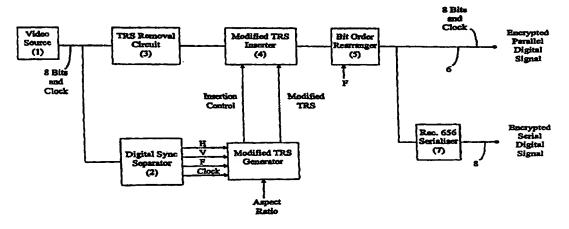
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(74) Agents: GARRATT, Peter, Douglas et al.; Mathys & Squire, 100 Gray's Inn Road, London WC1X 8AL (GB). (81) Designated States: AU, CA, JP, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

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Without international search report and to be republished upon receipt of that report.

(54) Title: DIGITAL VIDEO PROCESSING



(57) Abstract

To inhibit unauthorised copying, the standard timing reference signals in a Rec 656 digital video signal are stripped out and replaced by a single timing reference per frame. Authorised equipment contains the processing to reconstruct the necessary timing from these timing references. Additionally, the digital words can be scrambled by bit re-ordering or by a more sophisticated process.

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INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference PDG/21095	FOR FURTHER see Notification of Transmittal of International Search Report (Form PCT/ISA/220) as well as, where applicable, item 5 below					
International application No.	International filing date (day/month/year)	(Earliest) Priority Date (day/month/year)				
PCT/GB 99/04245	15/12/1999	15/12/1998				
SNELL & WILCOX LIMITED et This International Search Report has been	cal.	nority and is transmitted to the applicant				
	_	report.				
 Basis of the report a. With regard to the language, the 	international search was carried out on the bas	sis of the international application in the				
language in which it was filed, ur	less otherwise indicated under this item. vas carried out on the basis of a translation of the state of the					
was carried out on the basis of the contained in the internation of the filed together with the internation of the furnished subsequently to the statement that the subsequent international application of	onal application in written form. ernational application in computer readable form this Authority in written form. this Authority in computer readble form. bsequently furnished written sequence listing das filed has been furnished.	n.				
	ind unsearchable (See Box I).					
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the text has been establis	ubmitted by the applicant. shed, according to Rule 38.2(b), by this Authorit e date of mailing of this international search rep lished with the abstract is Figure No.	y as it appears in Box III. The applicant may, ort, submit comments to this Authority.				
as suggested by the appl		None of the figures.				
because the applicant fai	led to suggest a figure.					
because this figure better	characterizes the invention.					



International Application No PCT/GB 99/04245

			
A. CLASSI IPC 7	IFICATION OF SUBJECT MATTER H04N7/169 H04N7/171		
	o International Patent Classification (IPC) or to both national class	ification and IPC	
	SEARCHED ocumentation searched (classification system followed by classific	cation symbols)	
IPC 7	HO4N	valion symbolicy	
Documental	tion searched other than minimum documentation to the extent the	at such documents are included in the fields s	earched
Electronic d	data base consulted during the international search (name of data	base and, where practical, search terms used	(b
WPI Da	ta, PAJ		
C. DOCUM	ENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the	relevant passages	Relevant to claim No.
A	US 5 757 910 A (RIM) 26 May 1998 (1998-05-26) column 1, line 52 -column 2, li	no 64	1-22
	column 1, 11the 52 -column 2, 11 column 3, line 47 -column 5, li figures 3-5		
P,A	EP 0 949 815 A (NEC CORPORATION 13 October 1999 (1999-10-13) column 6, line 6 -column 7, line		15-22
	figure 1	C 11,	
Furth	her documents are listed in the continuation of box C.	Patent family members are listed	in annex.
° Special cat	tegories of cited documents:	"T" later document published after the inte	
	ent defining the general state of the art which is not lered to be of particular relevance	or priority date and not in conflict with cited to understand the principle or the invention	the application but
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other n	neans ent published prior to the international filing date but	ments, such combination being obvious in the art.	•
	nan the priority date claimed actual completion of the international search	"&" document member of the same patent Date of mailing of the international sea	<u> </u>
	0 July 2000	14/07/2000	•
Name and m	nailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2	Authorized officer	
	NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Dudley, C	

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Information on patent family members

International Appli	cation No
PCT/GB 99/	04245

, Patent document cited in search repor	t	Publication date		Patent family member(s)	Publication date
US 5757910	Α	26-05-1998	KR KR	9611031 B 9708408 B	16-08-1996 23-05-1997
EP 0949815	Α	13-10-1999	JP	11298878 A	29-10-1999